



LOGIC DESIGN 2

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Introduction

- Instructor
 - Nguyen Quang Huy huynguyen@cse.hcmut.edu.vn
- Course Page
 - Sakai (announcement, exercises, forum...)
 - Visit regularly for updates
- Evaluation
 - 30% midterm exam
 - 30% assignment (lab)
 - 40% final exam

Content

LOGIC DESIGN 1

Digital systems

Boolean

Combinational logic

Sequential logic

LOGIC DESIGN 2

MSI logic circuits

Memory

ADC / DAC

Logic family

MICRO-CONTROLLER

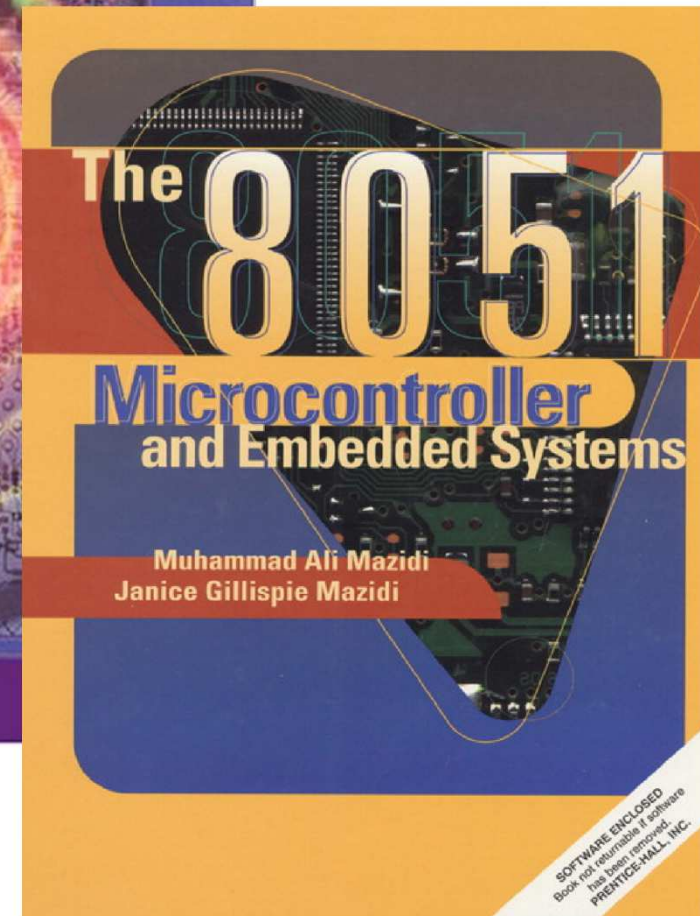
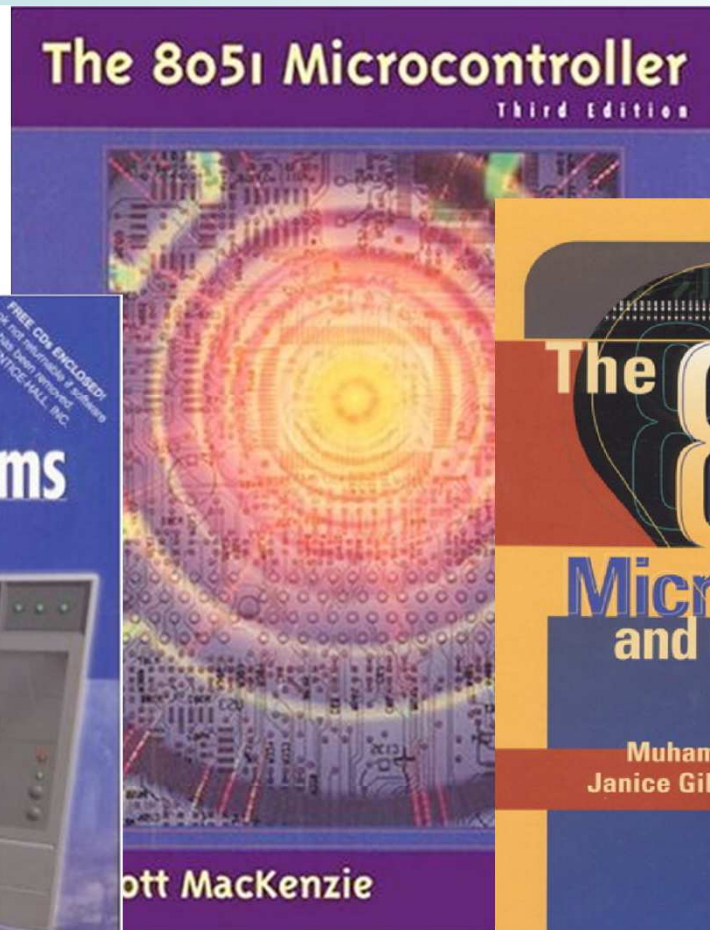
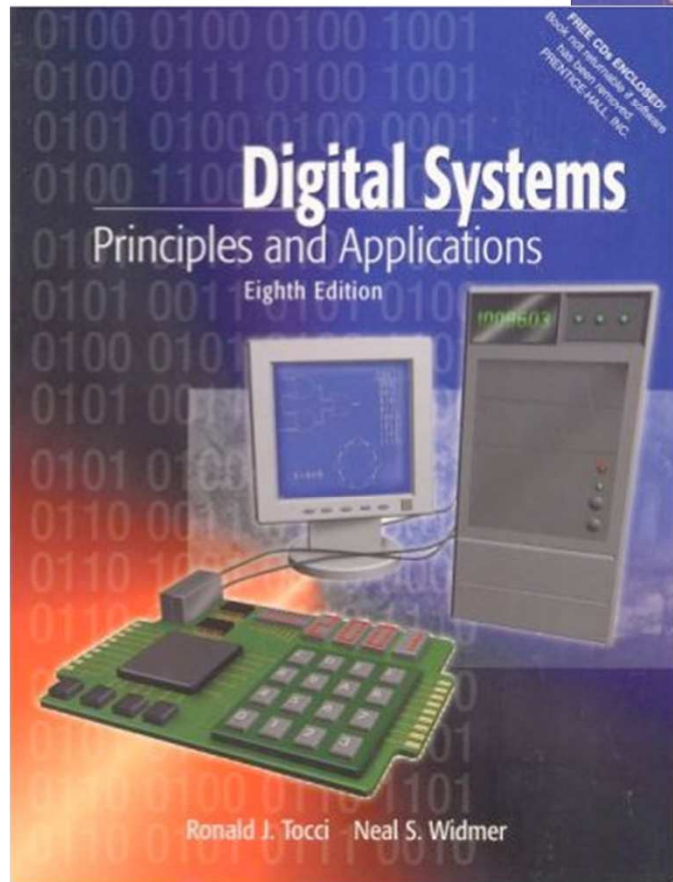
Textbooks

- Digital Systems
 - “*Digital Systems, Principles and Applications,*” R.J. Tocci, Prentice Hall
 - Chapter 8 - 11
 - “*The 8051 Microcontroller and Embedded Systems Using Assembly and C*” - Muhammad Ali Mazidi, Janice Gillispie Mazidi, Rolin D.McKinlay

Textbooks

- 8051 Microcontroller
 - *“The 8051 Microcontroller”* - I. Scott Mackenzie, Prentice-Hall
 - *“Họ vi điều khiển 8051”*, Tống Văn On, Hoàng Đức Hải

Textbooks



How to study

Books for you

Slides for me

- Read books more
- Do exercises
- Verify with tool

Tool

- Software
 - Circuitmaker
 - Proteus
- Hardware
 - Breadboard

